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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/815,427	03/31/2004	Peter G. Tolchinsky	42P18279	7622
8791	7590 10/23/2006		EXAMINER	
BLAKELY SOKOLOFF TAYLOR & ZAFMAN			KUNEMUND, ROBERT M	
12400 WILSH SEVENTH FI	IIRE BOULEVARD LOOR		ART UNIT	PAPER NUMBER
LOS ANGEL	ES, CA 90025-1030		1722	
		•	DATE MAILED: 10/23/2006	6

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	Applicant(s)					
Office Action Summary		10/815,427	TOLCHINSKY ET AL.					
		Examiner	Art Unit					
		Robert M. Kunemund	1722					
Period fo	The MAILING DATE of this communication aport.	ppears on the cover sheet with the c	orrespondence ad	Idress				
WHI(- Exte after - If NO - Failu Any	IORTENED STATUTORY PERIOD FOR REPI CHEVER IS LONGER, FROM THE MAILING I ensions of time may be available under the provisions of 37 CFR 1. or SIX (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory period ure to reply within the set or extended period for reply will, by stature reply received by the Office later than three months after the mailined patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION .136(a). In no event, however, may a reply be tind d will apply and will expire SIX (6) MONTHS from te, cause the application to become ABANDONE	N. nely filed the mailing date of this co					
Status								
1)⊠	Responsive to communication(s) filed on <u>02</u> A	August 2006.						
2a)⊠	This action is FINAL . 2b) Thi	is action is non-final.						
3)[Since this application is in condition for allowa	•		e merits is				
	closed in accordance with the practice under	Ex parte Quayle, 1935 C.D. 11, 45	53 O.G. 213.					
Disposit	ion of Claims							
4)⊠	Claim(s) <u>1-3,5-21 and 23-29</u> is/are pending in	the application.						
	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)	Claim(s) is/are allowed.							
6)⊠	6)⊠ Claim(s) <u>1-3, 5-21 and 23-29</u> is/are rejected.							
7)	Claim(s) is/are objected to.			•				
8)[Claim(s) are subject to restriction and/	or election requirement.						
Applicat	ion Papers							
9)[]	The specification is objected to by the Examin	er						
·	10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.							
٠٠/	Applicant may not request that any objection to the	•						
	Replacement drawing sheet(s) including the correct			FR 1 121(d)				
11)□	The oath or declaration is objected to by the E	• • • • • • • • • • • • • • • • • • • •	•	, ,				
	under 35 U.S.C. § 119							
_	~ ,			•				
	Acknowledgment is made of a claim for foreign	n priority under 35 U.S.C. § 119(a))-(d) or (f).					
a)	☐ All b)☐ Some * c)☐ None of:							
	1. Certified copies of the priority documen							
	2. Certified copies of the priority documen	• •	·					
	3. Copies of the certified copies of the price		ed in this National	Stage				
	application from the International Burea	, ,,,						
* See the attached detailed Office action for a list of the certified copies not received.								
Attachmen	it(s)			i				
_	ce of References Cited (PTO-892)	4) Interview Summary	(PTO-413)					
2) 🔲 Notic	ce of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	ate					
3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date 5) Notice of Informal Patent Application 6) Other:								
Paper No(s)/Mail Date 6) Uther:								

Application/Control Number: 10/815,427

Art Unit: 1722

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 1 to 3, 6, 7 and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Oida et al.

The Oida et al reference teaches a method and wafer, which is cut, form an ingot, note entire reference. The ingot is placed in a cutting device and sawed to create wafers or substrates. The cutting is done at angles, which do not include 90. This changes the wafer properties, note col 5 lines 20 to 35. The prior art differs from the instant claims in the specific orientations. However, in the absence of unexpected

results, it would have been obvious to one of ordinary skill in the art to determine through routine experimentation the optimum, operable orientations to be cut in the Oida et al reference in order to create the desired wafer orientation as the reference does teach that different orientation can be use and are within the skill of the art.

Claims 8, 9, 10, and 23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sato (6,375,738) in view of Oida et al.

The Sato et al reference teaches a method of creating an SOI structure, note entire reference. On a first substrate, a layer or layers can be growth. The grown layers can be epitaxial silicon and silicon dioxide, note examples. The structure is then bonded to another substrate. At a point in the first substrate which as been changed structurally, a separation is done so that the two substrates are separated with the grown structure on the second substrate, note examples. The sole difference between the instant claims and the prior art is the ingot cutting orientations. However, the Oida et al reference teaches cutting ingots at non-90 angles, note cite supra. It would have been obvious to one of ordinary skill in the art to modify the Sato reference by the teachings of the Oida et al reference to use different orientation in order to place a epitaxially structure of one orientation on a substrate with a different one changing the device properties.

Claims 11 to 16 and 24 to 29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sato (6,375,738) in view of Oida et al.

The Oida et al and Sato references are relied on for the same reasons as stated, supra, and differs from the instant claims in the removal step and orientation. However,

in the absence of unexpected results, it would have been obvious to one of ordinary skill in the art to determine through routine experimentation the optimum, operable orientations and removal in the Sato and Oida et al references in order to create the desired wafer orientation with no damage as the reference does teach that different orientation can be use and are within the skill of the art.

Claims 5 and 17 to 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sato (6,375,738) in view of Oida et al. and Henley et al (6,558,802)

The Oida et al and Sato references are relied on for the same reasons as stated, supra, and differs from the instant claims in the ion implantation. However, the Henley et al reference teaches ion implantation to create a damage layer to aid in separation of substrates, note col.3. It would have been obvious to one of ordinary skill in the art to modify the Sato reference by the teachings of the Henley et al reference to ion implant in order to increase the ease of separation placing less strain on the structure.

Response to Applicants' Argument

Applicant's arguments filed August 2, 2006 have been fully considered but they are not persuasive.

Applicants' argument concerning claims 1-3, 5-9 and 17-19 is noted. However, as admitted in the specification, under the background section, notching wafers is within the skill of the art. Notching is done to show the orientation of the wafer. Thus, it would have been within the skill of the art to notch off zero as the cutting and hence surface orientation is not zero.

Applicants' argument concerning claims 10 to 16 and 23 to 27 has been considered and not deemed persuasive. Since the layers are already grown, it is well within the skill of the art to use different orientations when bonding. The bonding process will not change the orientation. Therefore, using different orientations can make different devices.

Applicants' argument concerning 20, 21, 28 and 29 is noted. However, the art does teach slicing at off angles, and thus does in fact teach the orientations claimed. Since the cut is off the orientation is also off. Further, the art does teach the slicing as claimed, and applicants admit that the slicing changes the properties. The prior art inherently meets the property claims.

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Robert M. Kunemund whose telephone number is 571-272-1464. The examiner can normally be reached on 8 hours.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Yogendra Gupta can be reached on 571-272-1312. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Robert M Kunemund Primary Examiner Art Unit 1722

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